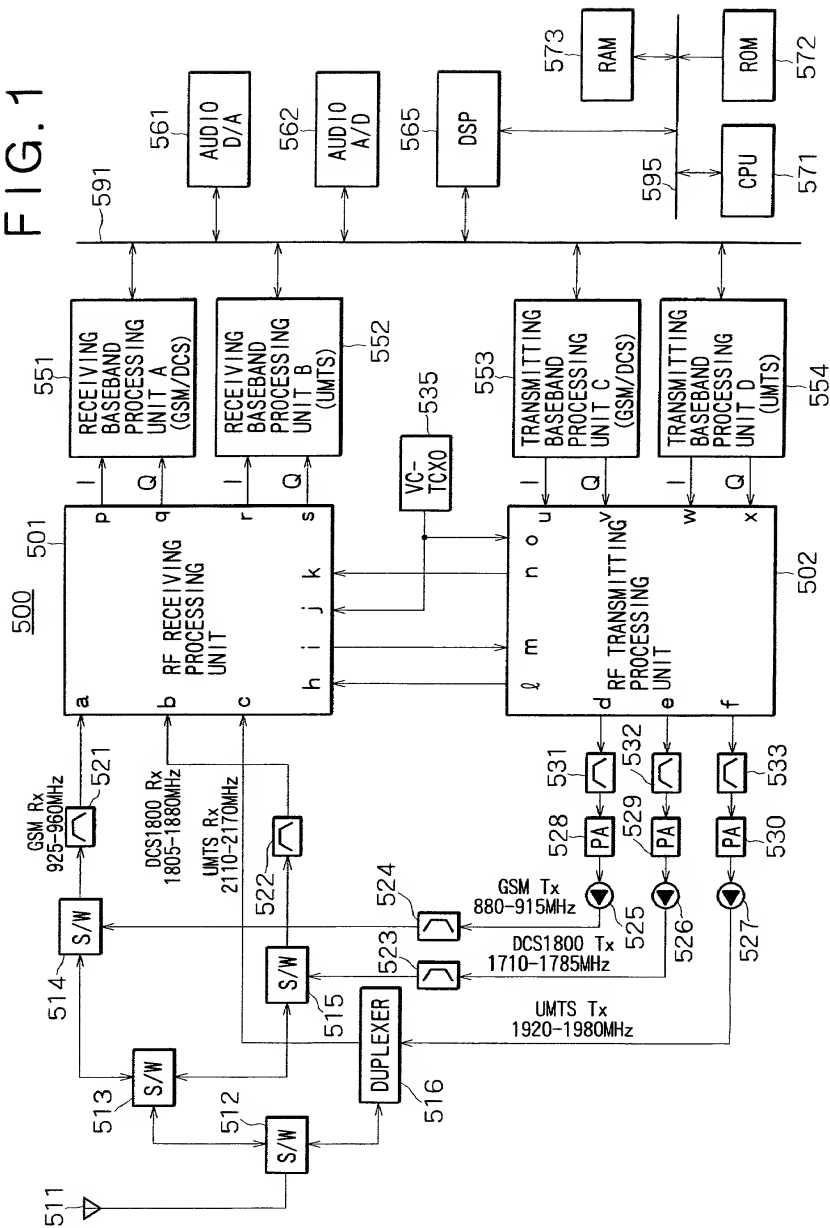


FIG. 1



501

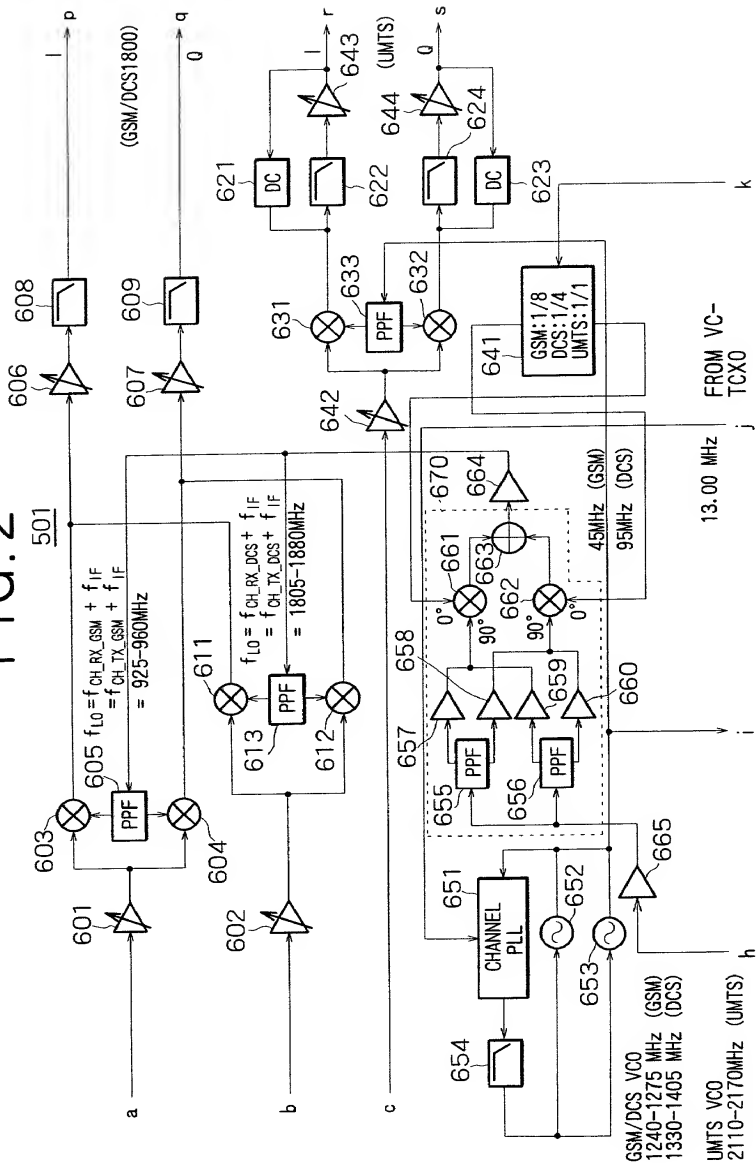
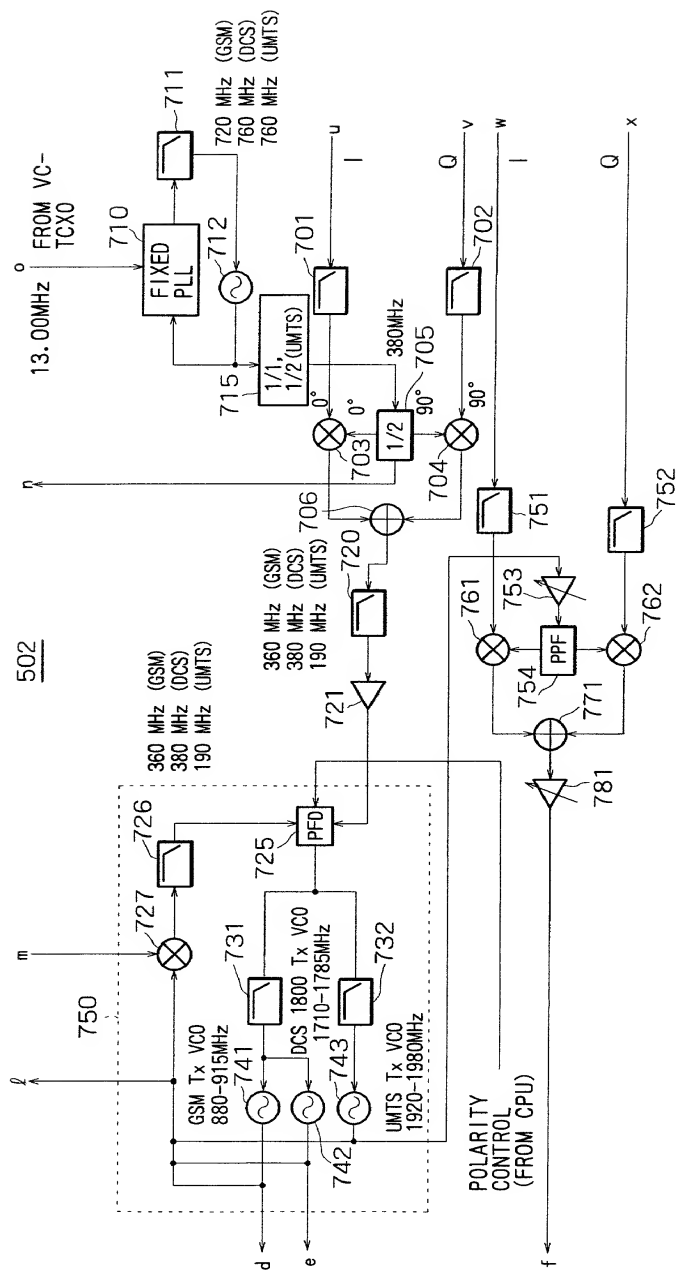


FIG. 3



502

POLARITY  
CONTROL  
(FROM CPU)

FIG. 4A

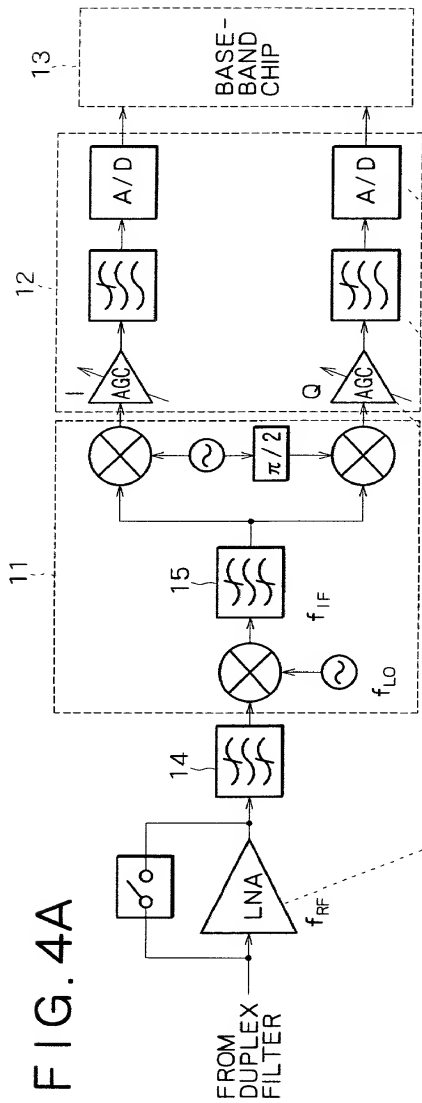


FIG. 4B

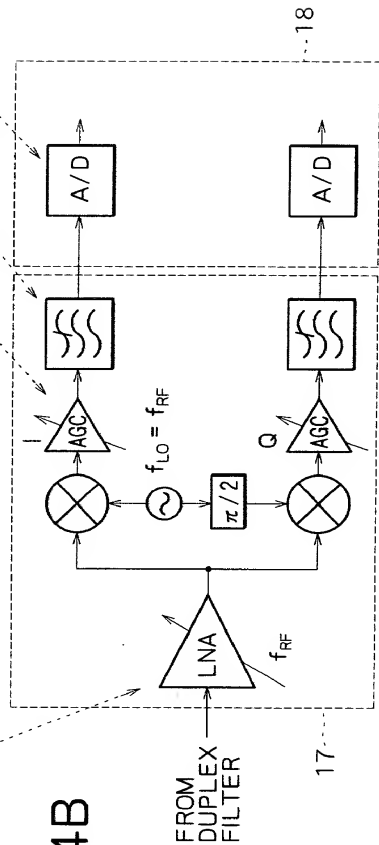
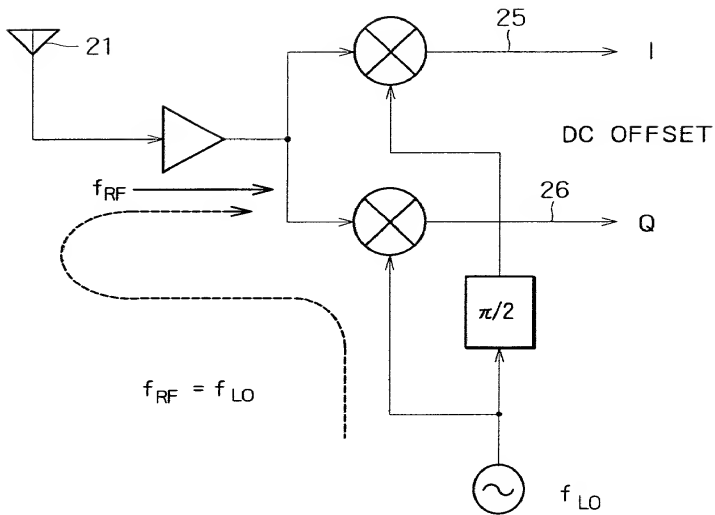
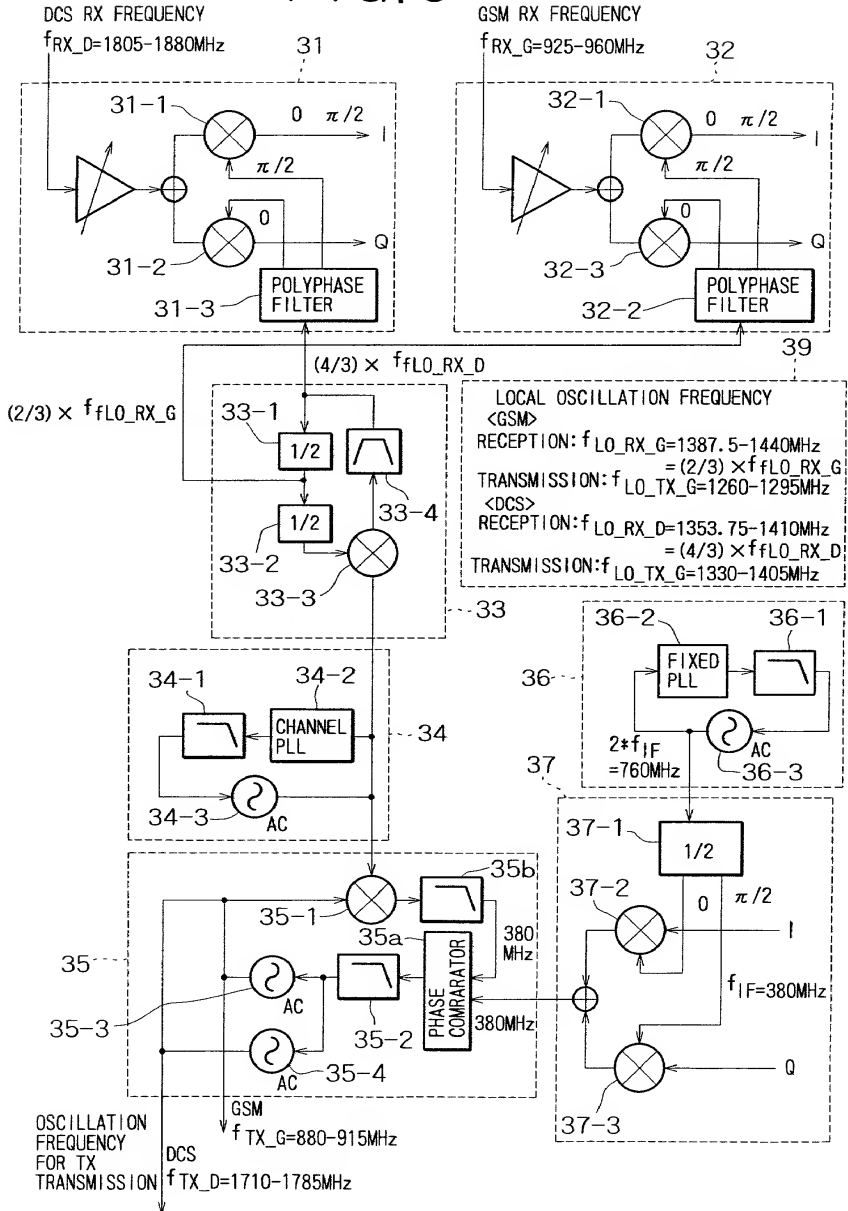


FIG. 5



# FIG. 6



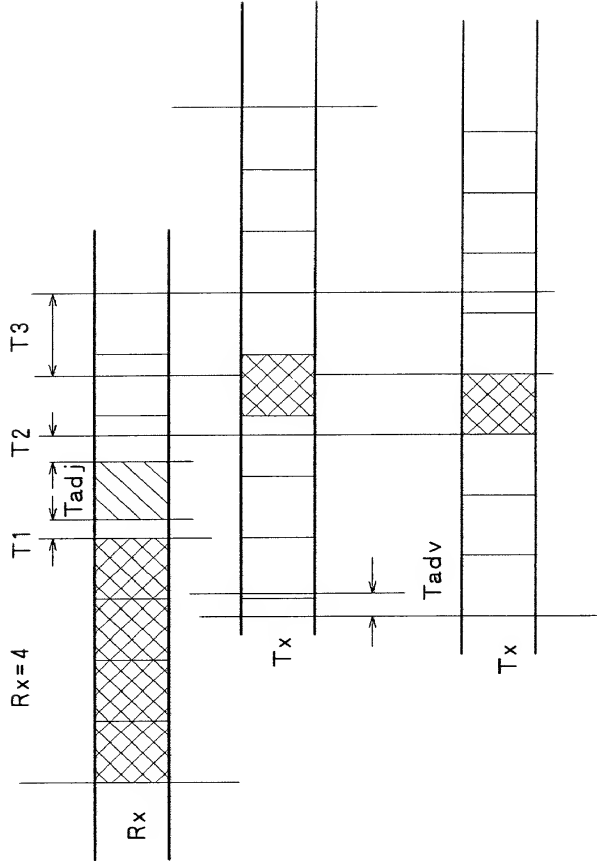


FIG. 7A

FIG. 7B

FIG. 7C